

Electronic Filing System (EFS) Data
Electronic Patent Application Submission
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09/681103
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EFS ID: 10816
Application ID: 09681103
Title of Invention: Logic Emulator with Routing
Chip providing Virtual Full-
Crossbar Interconnect
First Named Inventor: Tan Tseng
Domestic/Foreign Application: Domestic Application
Filing Date: null
Effective Receipt Date: 2001-01-05
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TRANSMITTAL FORM



Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number:

LX-5

Logic Emulator with Routing Chip providing Virtual Full-Crossbar Interconnect

First Named Inventor: Tan Tseng

SUBMITTED BY

Name: Stuart T Auvinen
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Date Signed: 20010105

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Attached Files:

bibd-transmittal	LX5exapds.xml
fee-transmittal	LX5exfee.xml
specification	LX5e.xml
declaration	DeclLX5.tif

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FEE TRANSMITTAL

Electronic Version 1.0.4

Stylesheet Version: 1.0

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Small Business Concern

TOTAL FEES AUTHORIZED: \$ 355

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Authorized Name:

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Electronic Signature Mark:

/es/stuauvinen

Date Signed:

20010105

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	201	\$ 355

Subtotal For Basic Filing Fee: \$ 355

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 20	203	\$ 9	0	\$ 0
Independent Claims: 3	202	\$ 40	0	\$ 0

[illegible]